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Call for Papers

The **IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems** is intended to provide a forum for exchanging ideas, and presenting research results and practical applications in the areas of design, test and diagnosis of microelectronic circuits and systems.

The **DDECS Workshop** series are organised by Central European countries: Czech Republic (1997, 2002), Poland (1998, 2003), Slovakia (2000) and Hungary (2001). DDECS 2004 is organised by the **Institute of Informatics** of the Slovak Academy of Sciences and sponsored by the **IEEE Computer Society - Test Technology Technical Council (TTTC)** and the IST-2000-30193 REASON Project - Research and Training Action for System on Chip Design.

Topics of interest include but are not limited to:

- ASIC/FPGA Design
- Bio-inspired Hardware
- Design Verification/Validation
- Formal Methods in System Design
- Hardware/Software Co-Design
- IP-based Design
- Logic Synthesis
- Physical Design
- Reconfigurable Computing
- System-on-a-Chip (SoC)
- Analog, Mixed-Signal and RF Test
- ATE Hardware and Software
- Built-in Self-Test (BIST)
- Design for Testability and Diagnosis
- Defect/Fault Tolerance and Reliability
- Embedded Test
- Memory and Processor Test
- MEMS Testing

Paper submissions: Prospective authors are cordially invited to submit original papers in English of maximum 8 pages. Electronic submission (using the workshop web page) in PostScript or PDF format is required. Please identify a contact author with complete mailing address, phone and fax numbers and e-mail address. Additionally, a special session for demonstration of tools and designs developed by students is planned to be organised (contributors have to submit max. 3 pages). Accepted papers will be included in the Workshop Proceedings.

Important dates:

- Submission deadline: **January 19, 2004**
- Notification of acceptance: **February 23, 2004**
- Camera-ready deadline: **March 15, 2004**

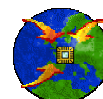
Workshop location: The Congress Centre *ACADEMIA* is situated at the foot of the Lomnický štít massif in the High Tatra Mountains about 2 km from the town Tatranská Lomnica. The hotel has the advantage of being located in a beautiful, natural setting on the border of the Tatra National Park while at the same time being easily accessible by road, train, bus and airplane (the distance from the airport and the major route in Poprad is 15 km).

For further information please contact the General Co-Chairs, the Programme Chair/Vice-Chair, the Organising Committee Chair or visit the DDECS 2004 web page.

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